



ESTD.1917

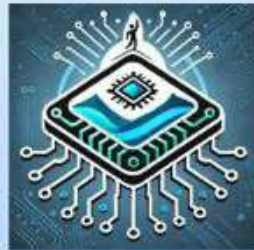


ESTD.1929

One Week Faculty Development Program/Workshop on

HURDLES AND HORIZONS IN THE INDIAN SEMICONDUCTOR IC DESIGN

21st to 26th October 2024



ORGANIZED BY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
UNIVERSITY COLLEGE OF ENGINEERING (AUTONOMOUS)
OSMANIA UNIVERSITY, HYDERABAD, INDIA.



RESOURCE PERSONS

- Prof. V. Ramgopal Rao**
Sr. Professor & Vice-Chancellor, Dept of EEE, BITS Pilani, India.
- Dr. Satya Gupta**
President, VLSI Society of INDIA.
- Dr. Alex James**
Dean Academic, Digital University, Kerala, India.
- Prof. Srihari Rao**
Professor, Department of ECE, MITW, Telangana, India.
- Dr. Deepak Mishra**
Head & Associate Project Director, TDS-01, SAC, ISRO, Ahmedabad, India.
- Dr. Ajith S**
Sr. Director of Engineering, Intel Corporation, Bangalore, India.
- Dr. Krishnakanth Avalur**
Director, Analog/Mixed-Signal R&D, AMS OSRAM, Hyderabad, India.
- Dr. Nanda Kishore Chavali**
Development Manager, MathWorks, Hyderabad, India.
- Mr. M. Manoj**
Scientist, Research Center of Imarat, India.
- Mr. Sanjeev Srivatsav**
Founder & CEO, ZOICX Technologies, Hyderabad, India.
- Dr. Balaji Jayaram**
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Mr. Md. Misbahuddin
Assistant Professor,
Dept. of ECE, UCE, OU.

Co-coordinator

Dr. M. Shyam Sunder
Associate Professor
Dept. of ECE, UCE, OU.

Venue:
Dept. of Electronics & Communication Engg.,
University College of Engineering(A),
Osmania University, Hyderabad.

Workshop Objective

The objective of the workshop is to explore the current challenges and opportunities in the Indian Semiconductor IC Design industry, providing insights into overcoming technical barriers and leveraging emerging trends for growth and innovation.

Who Can Participate?

- Executives, Engineering and Researchers from Manufacturing, Service and Govt. Organizations including R&D Laboratories.
- Faculty members of Engineering Colleges.
- Students and Research Scholars from academic institutions and Technical institutions.

Fee Details

Industry Person: ₹5000/-

Academic Faculty: ₹2500/-

Students and Fulltime Research Scholars : ₹1000/-

ACCOMMODATION : On request Accommodation will be provided for few participants (first-come, first-served basis) in the campus on payment.

How to Register?

Google Form link:

<https://forms.gle/9Kz5J5Dd6rttWSp926>

UPI QR Code:



Payment Details:

Acc Name: HEAD DEPT OF ELEC AND COMMNCNS ENGG

Acc No.: 52198271467

Bank: State Bank of India

Branch: Osmania University

IFSC: SBIN0020071

Whom to Contact?

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- Mr. T.M Praneeth Naidu 9014240241**
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About the C2S Program

MeitY sanctioned a Prestigious Chip to Start-up (C2S) program to develop ADPLL for BLE applications and train Manpower in Semiconductor Design. It is a pleasure to inform you that the College is carrying out Research activities using full fledged VLSI CAD Tools and FPGA boards, funded by MeitY that leads to Marketable Products/Technology Transfers.

About the Department

The department of Electronics & Communication Engineering (ECE) was established in the year 1959. Professor K. K Nair was the founder Head of the department. He transformed the department as one of the excellent teaching and research institutes in India. This attracted good students as well as good faculty members. The department is offering one UG programme B.E. (ECE) and Four PG Programs namely Digital Systems, Systems and Signal Processing, Microwave & Radar Engineering and VLSI & Embedded Systems and a PhD Program.

The department has been in forefront for research and training activities in the areas of VLSI, Embedded Systems, Digital Signal Processing, Image Processing, Microwave Engineering and Modern Telecommunications. The department successfully completed various projects funded by different agencies such as World Bank, SWISS Development Corporation, DST, MeitY, DRDO, ISRO, AICTE and UGC etc. The Biomedical Engineering (BME) department and the Navigational Electronics Research and Training facility (NERTU), a research facility at Osmania University, were established by the ECE department.

About the College

The University College of Engineering has the distinction of being the oldest and the biggest among the Engineering Colleges of the Telangana State. It was established in the year 1929, eleven years after the formation of Osmania University. The college was the sixth Engineering College to be established in the whole of British India. The college moved to its present permanent building in the year 1947. The college was given autonomous status in 1994. The college offers seven undergraduate programs, twenty-five postgraduate programs which are aimed to match current industry needs and self-employment. It also offers research programs leading to PhD in six different departments.

About the University

Osmania University, established in 1917, is the seventh oldest in India, the third oldest in south India and the first to be established in the erstwhile princely state of Hyderabad. Through out its existence of over Ten decades, it has shown remarkable progress and sustained an integrated development of all faculties. It has significantly contributed to the academic and economic development of not only the region but also of the Country. Its alumni have distinguished themselves nationally and internationally in various spheres of life and are spread far and wide around the world.

The University has a vision of developing, enhancing, and improving the quality of human resources to meet the challenges of regional, national and global socio-economic changes. It's mission is to achieve excellence in teaching and research and to create opportunities for the students to contribute to the national and regional development. Osmania University is re-accredited by the National Assessment and Accreditation Council (an Autonomous Institution of the University Grants Commission) as 'A+' Grade University.